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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**APPLICANT:** Young-Mi TAK                      **DOCKET NO.** 8071-49 (OPP021241US)  
**SERIAL NO:** 10/718,309                      **GROUP:** Art Unit 2814  
**FILED:** November 20, 2003                      **EXAMINER:** Ginette Peralta  
**FOR:** THIN FILM TRANSISTOR ARRAY PANEL  
AND MANUFACTURING METHOD THEREOF

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

**RESPONSE TO RESTRICTION REQUIREMENT**

Sir:

In response to the Office Action dated June 29, 2005, applicant provisionally elects the claims of Group I (claim nos. 1-9), with traverse. While the device of Group I and the method of Group II may be distinct for the reasons set forth by the Examiner, applicant believes that simultaneous examination will not present an undue burden. For example, the claims of Group I are drawn to a thin film transistor array panel and the claims of Group II are drawn to a method of manufacturing a thin film transistor array panel. The two Groups of claims include subject matters that can be classified commonly in class 257.

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**CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)**

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, postpaid in an envelope, addressed to the: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on July 26, 2005.

Dated: \_\_\_\_\_


*7/26/05*

*[Signature]*  
\_\_\_\_\_  
Frank Chau

Under such circumstances, the Examiner is encouraged to maintain all claims in the same application. See, MPEP § 803.

Early and favorable consideration of this application are earnestly solicited.

Respectfully submitted,

By:   
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